

5/5/2006

PTO/SB/06A (10-01)
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 US Patent & Trademark Office, U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/765,301
Filing Date	January 27, 2004
First Named Inventor	Leslie, Terrence
Group Art Unit	2815
Examiner Name	Landau, Matthew

Sheet 1 of 1

Attorney Docket No: 303.860US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-6,687,146	02/03/2004	Kurjanowicz, Wlodek , et al.	01/31/2002

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
me		DEFFREE, SUSAN , "IBM proves vertical transistor DRAM at 70nm", <u>Electronic News</u> , (June 10, 2003), 2 pgs	
me		HERGENROTHER, J M., et al., "The vertical replacement-gate (VRG) MOSFET: a 50-nm vertical MOSFET with lithography-independent gate length", <u>Bell Laboratories, Lucent Technologies, Murray Hill</u> , 4pgs (NO DATE) ✓	
me		ISA, "IBM claims speediest silicon transistor", http://www.isa.org/Template.cfm?Section=Communities&template=/TaggedPage/DetailDisplay.cfm&ContentID=20445 , ISA - The Instrumentation, Systems and Automation Society, (November 4, 2002), 2 pgs	
me		LUCENT TECHNOLOGIES, "Revolutionary transistor design turns the silicon world on end", http://www.bell-labs.com/news/1999/november/15/1.html , Lucent Technologies, Bell Labs Innovations, (1999), 3 pgs	
me		LUCENT TECHNOLOGIES, "Revolutionary transistor turns silicon world on end", (2000), 2 pgs	
me		NING, T H., "Why BICMOS and SOI BiCMoS?", <u>IBM</u> , 0018-8646, (11.29.01), 7 pgs	

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Matthew C. J. J.

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<i>Me</i>		GEPPERT, LINDA, "Triple Gate Double Play: Transistors with three gates and 3-D geometries will replace today's devices in a few years", <u>IEEE Spectrum</u> , (November 2002), 18	

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Matthew C. Fisher

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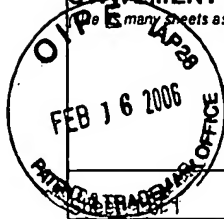
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Attorney Docket No: 303.860US1

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
<i>ML</i>	US-2006/0006444 A1	01/12/2006	Leslie, T. C.	08/31/2005
	US-5,244,824	09/14/1993	Sivan, R. D.	09/05/1990
	US-5,312,782	05/17/1994	Miyazawa, Yoshihiro	11/21/1991
	US-6,261,894	07/17/2001	Mandelman, Jack A., et al.	11/03/2000
	US-6,309,919	10/30/2001	Liu, Yowjuang W., et al.	01/25/1999
	US-6,316,309	11/01/2001	Holmes, Steven J., et al.	07/26/2000
	US-6,335,239	01/01/2002	Agahi, Farid, et al.	08/01/2000
<i>ML</i>	US-6,399,979	06/04/2002	Noble, Wendell P., et al.	06/16/2000

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
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